Total No. of Questions : 8] *[Total No. of Printed Pages : 2*

Roll No .....

CS-6001-CBGS

B.E. VI Semester

Examination, December 2020

Choice Based Grading System (CBGS)

Advanced Computer Architecture

Time : Three Hours

Maximum Marks : 70

Note: i) Attempt any five questions.

ii) All questions carry equal marks.

1. a) Describe about Flynn's classification.
   b) Explain data flow and demand driven mechanism.

2. a) Write system attributes to performance and explain.
   b) Draw dynamic interconnection networks and explain about its uses.

3. a) What is instruction set? Differentiate RISC and CISC architectures.
   b) What is meant by interleaving? Explain interleaved memory organization?

4. a) Write the role of Arbitration Transaction and Interrupt.
   b) Write comparison between CISC scalar processors and RISC scalar processors.

5. a) Discuss about cache coherence problem and its solution.
   b) Write principles of vector processing.

6. a) With neat diagram, explain the basic structure of a centralized shared memory and distributed shared memory multiprocessor.
   b) Discuss the issues involved in multithreading and how are they resolved?

7. a) Give a brief note on vector super computer.
   b) Explain shared variable model.

8. Write short notes on following (Any three):
   a) VLIW architecture
   b) SIMD Super computer
   c) Snoopy bus Protocol
   d) Tomosulo's algorithm

******